

**Amendments to the Abstract:**

An apparatus for generating pseudo-noise codes adapted for advancing or retarding pseudo-noise chips generated therefrom within one clock in a radio communication network ~~base-based~~ on code division multiple access ~~(CDMA)(CDMA)~~. ~~the~~The apparatus includes a control unit for outputting a control signal for the normal state or a PN chip advance; a plurality of MUXs for outputting an output value of the next state for a normal operation or an output value of a PN chip advance as an output signal in response to the control signal of the control unit; and a plurality of shift registers for outputting PN chip codes of the next or the second next state during one system clock time period in response to said MUXs. Further, the input end of each of the shift registers is connected to the output end of each MUX.